

3' 15

ANALYZER

sensor input 11

comparator 12

signal gen. 14

15

Fig. 1

Fig. 2

• ENCODING SYSTEM

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200209859-1



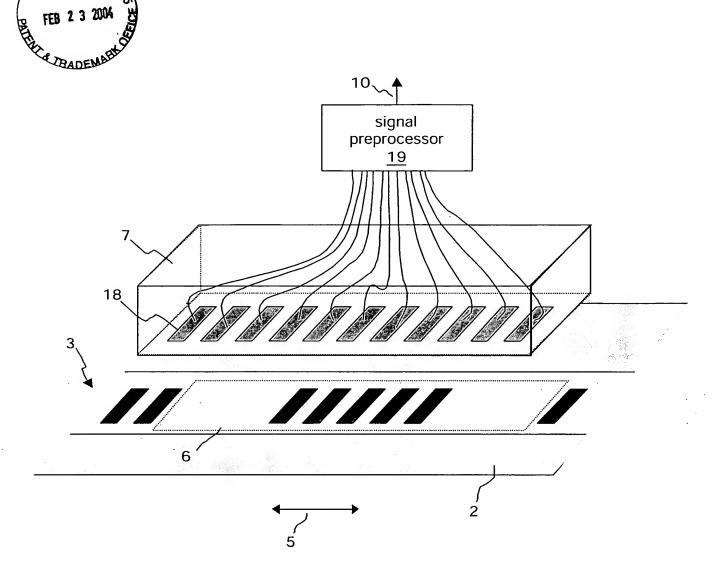


Fig. 3



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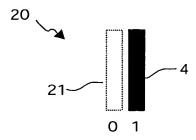


Fig. 4a

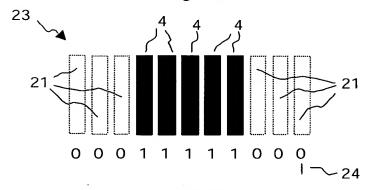


Fig. 4b

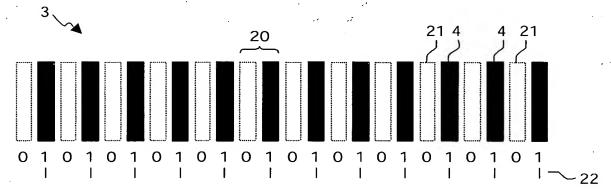


Fig. 4c

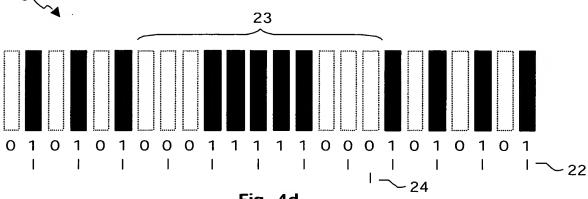


Fig. 4d



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	viewed pattern		ncremental signal	index signal
P1		10101010101	. 1	0
P2		01010101010	0	0
P3		00101010101	. 1	0
P4		00010101010	0	0
P5		10001010101	. 1	0
P6		11000101010	0	0
P7		11100010101	. 1	0
P8		11110001010	0	0
P9		11111000101	1	0
P10		01111100010	0 ·	0
P11		00111110001	1	. 0
P12		00011111000	0	1
P13		10001111100	1	0
P14		01000111110	0	0
P15		10100011111	. 1	0
P16		01010001111	. 0	0
P17		10101000111	. 1	0
P18		01010100011	. 0	0
P19		10101010001	. 1	0
P20		01010101000	0	0
P21		10101010100	1	0
P2		01010101010	0	0
P1		10101010101	. 1	0

Fig. 5



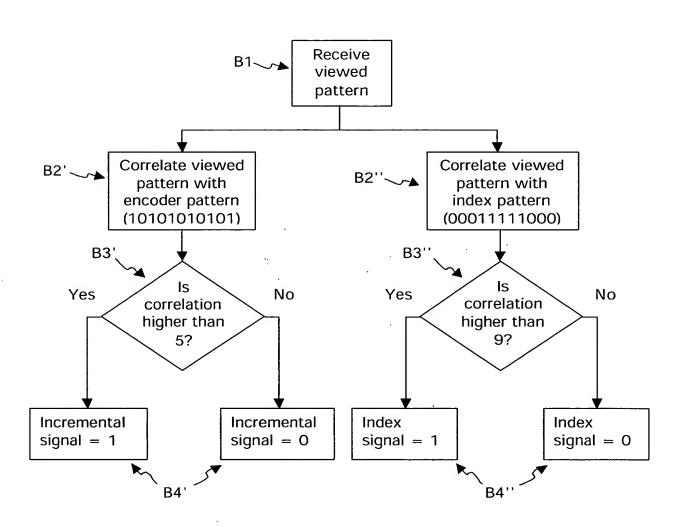


Fig. 6

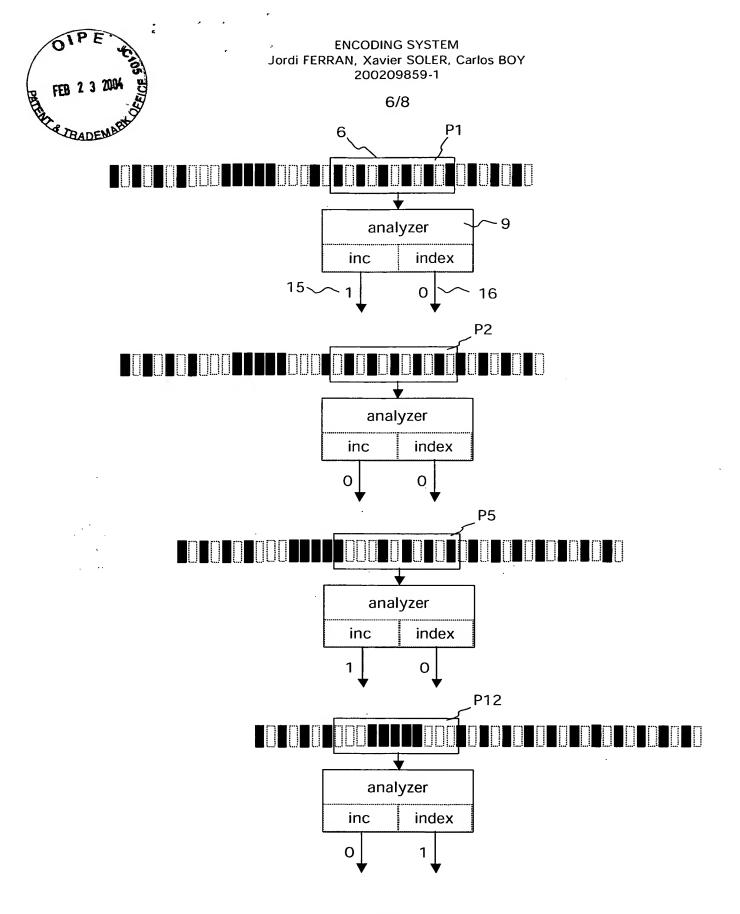
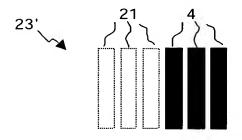


Fig. 7

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	viewe	ed pattern	incrementa signal	l index signal
R1		010101	1	0
R2		101010	0	0
R3		110101	1	0
R4		111010	0	1
R5		011101	1	0
R6		001110	. Ž 0	0
R7		000111	1	1
R8		100011	0	0
R9		010001	1	0
R10		101000	0	0
R11		010100	1	0
R2		101010	0	0
R1		010101	1	0

Fig. 8





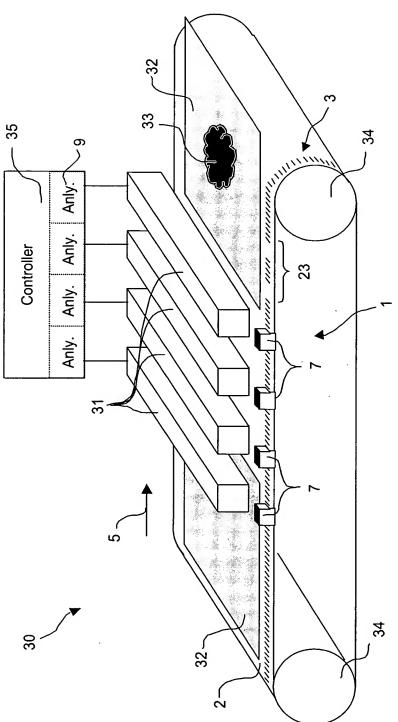


Fig. 9